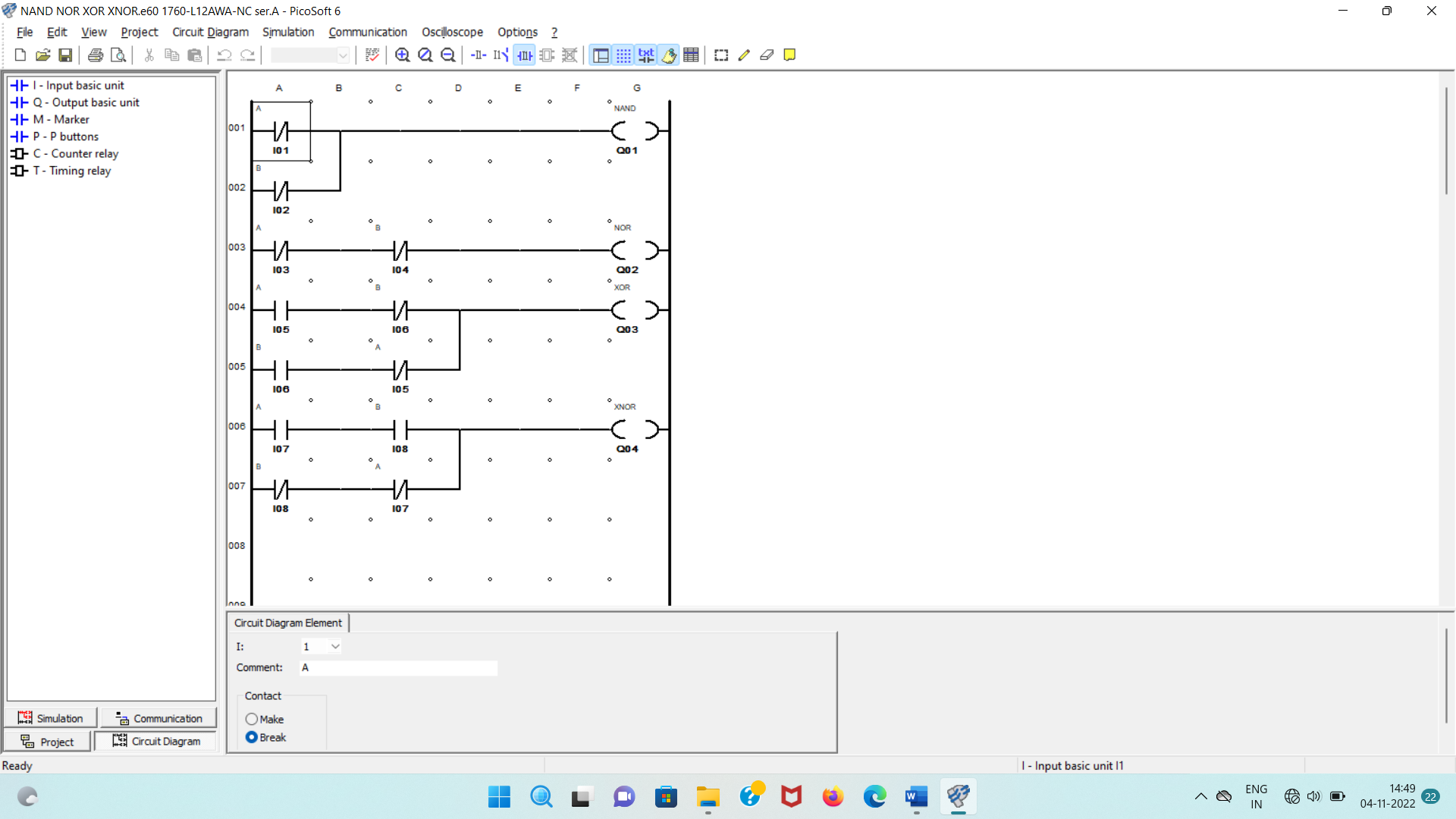
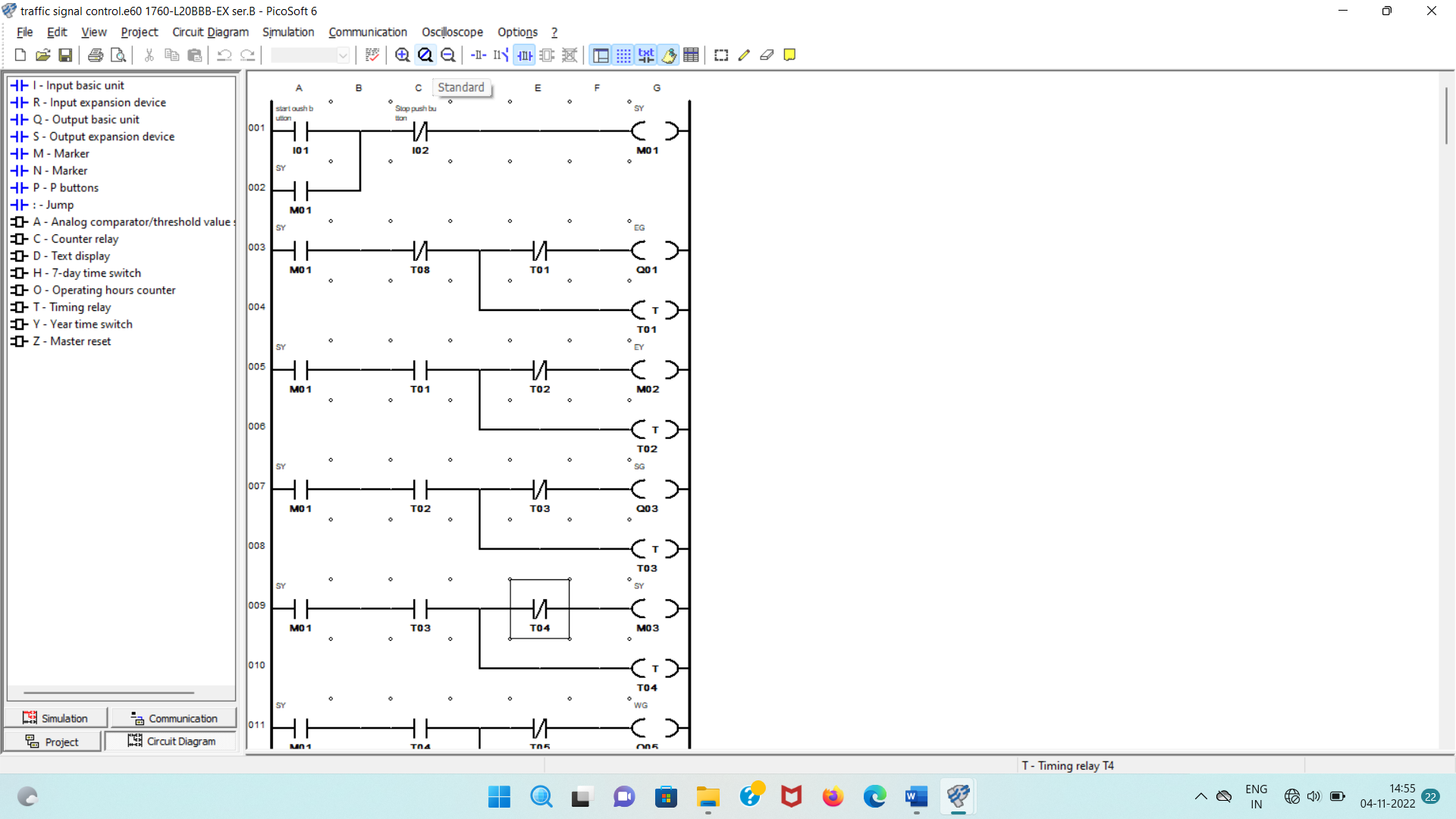
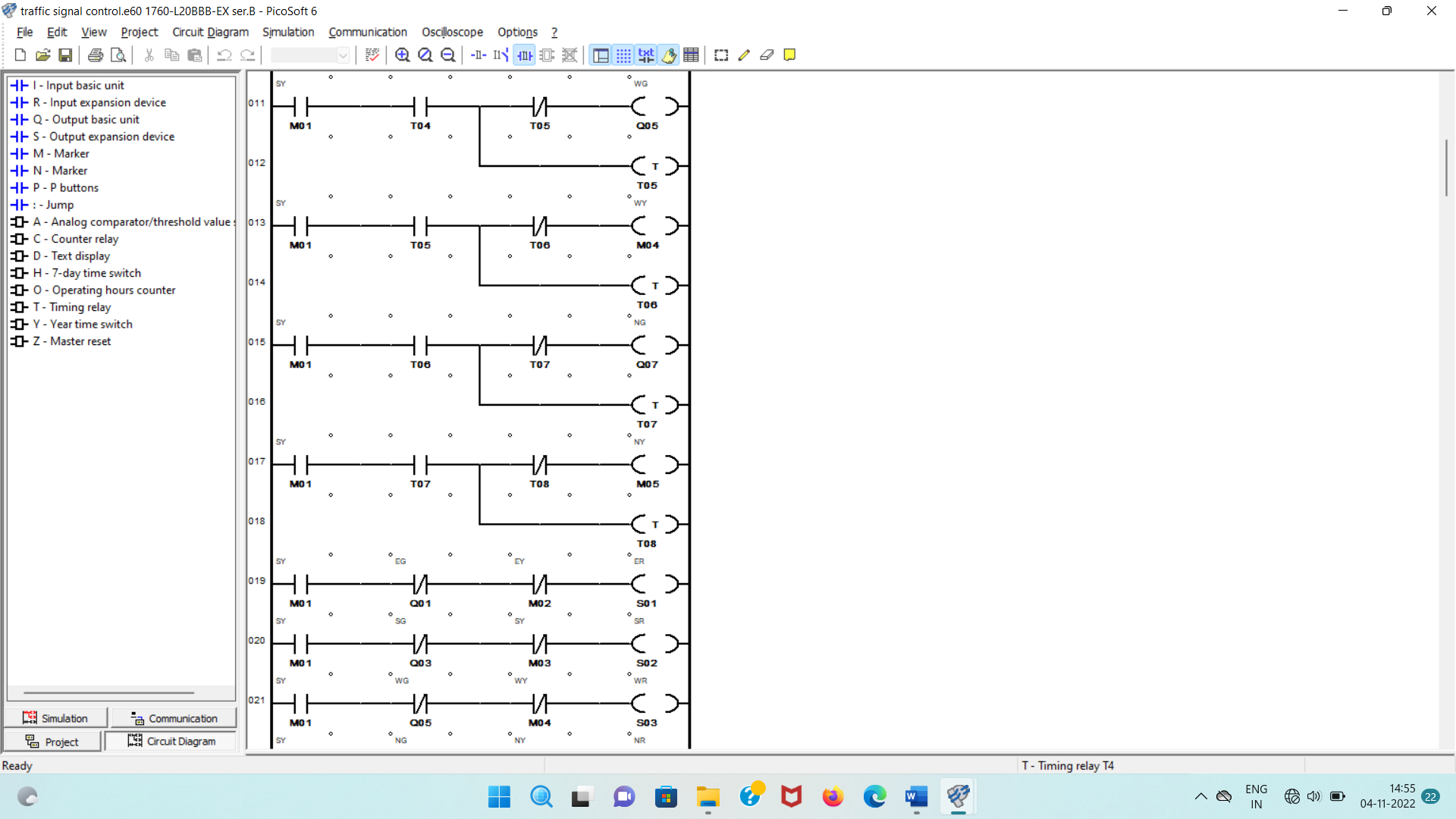


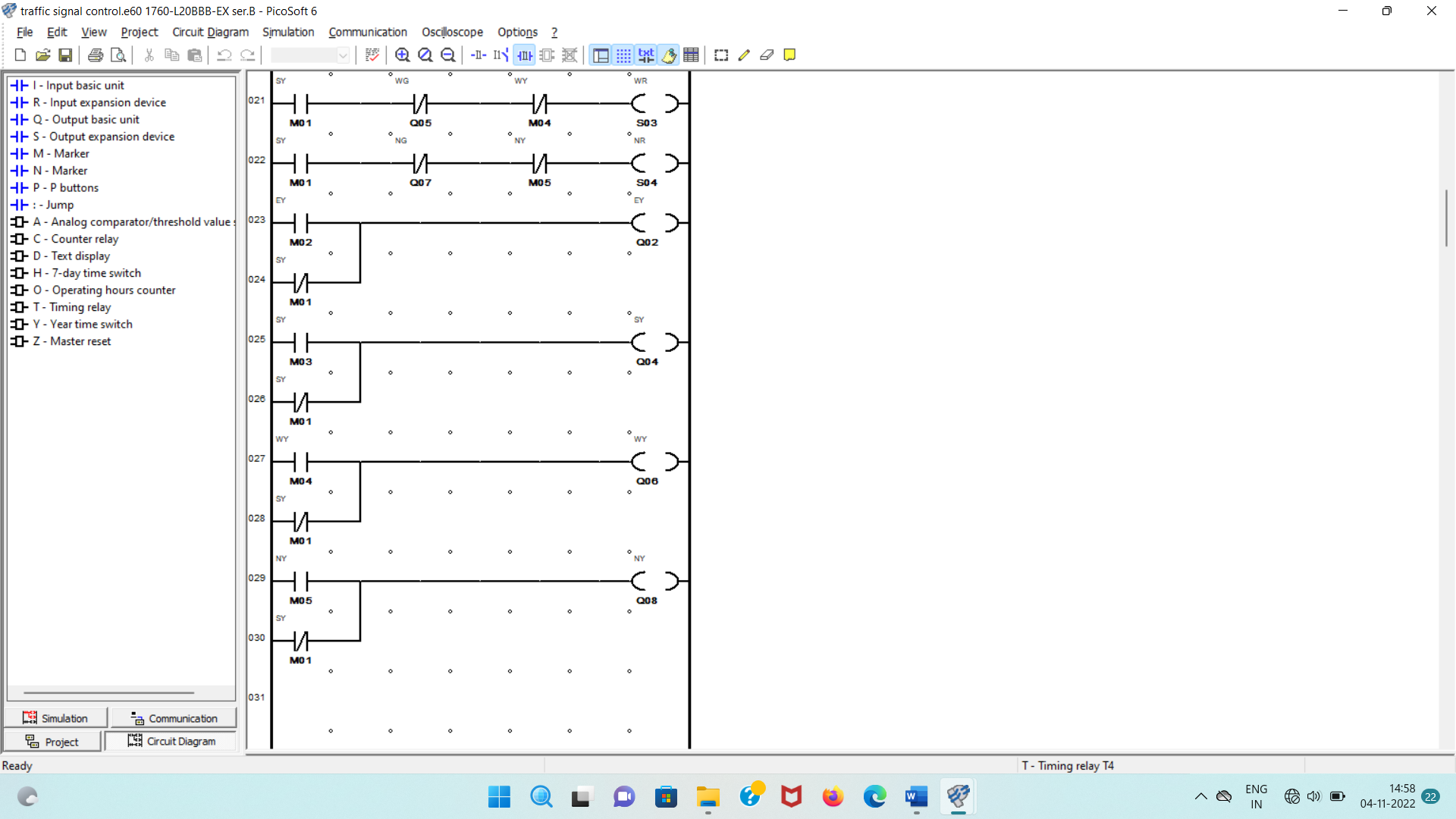
**LOGIC GATES**



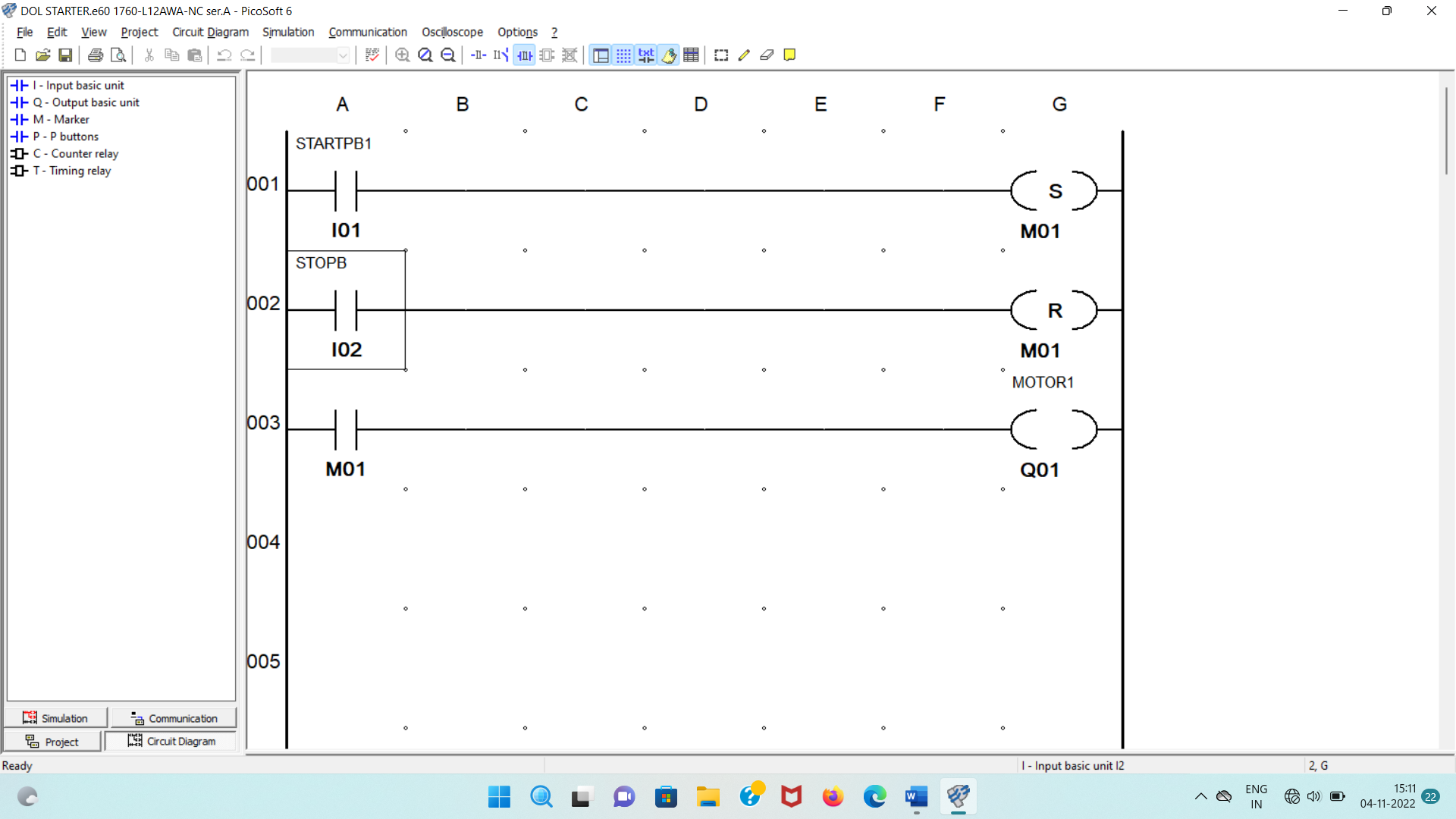
**TRAFFIC LIGHT SIGNAL CONTROL**





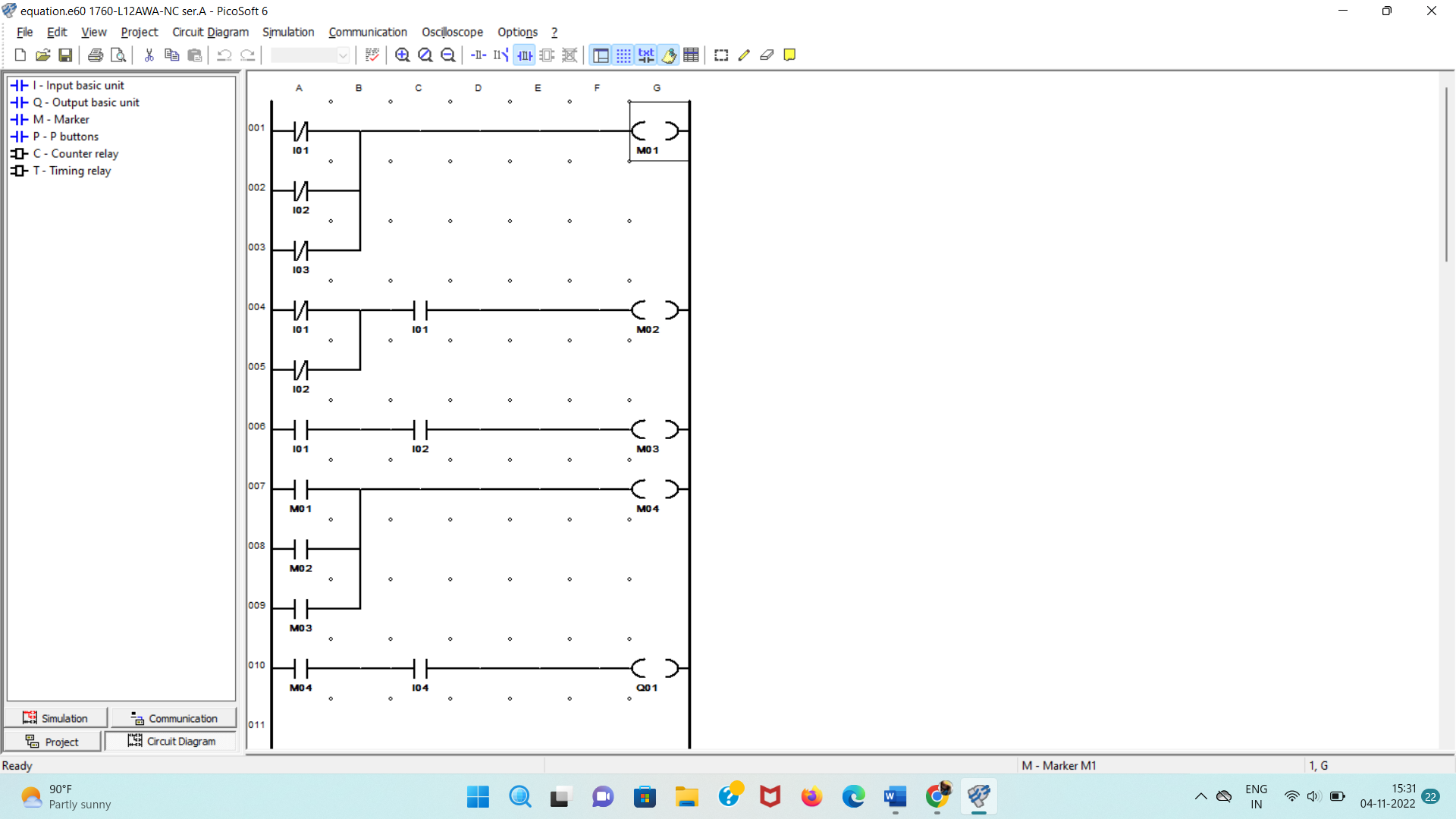


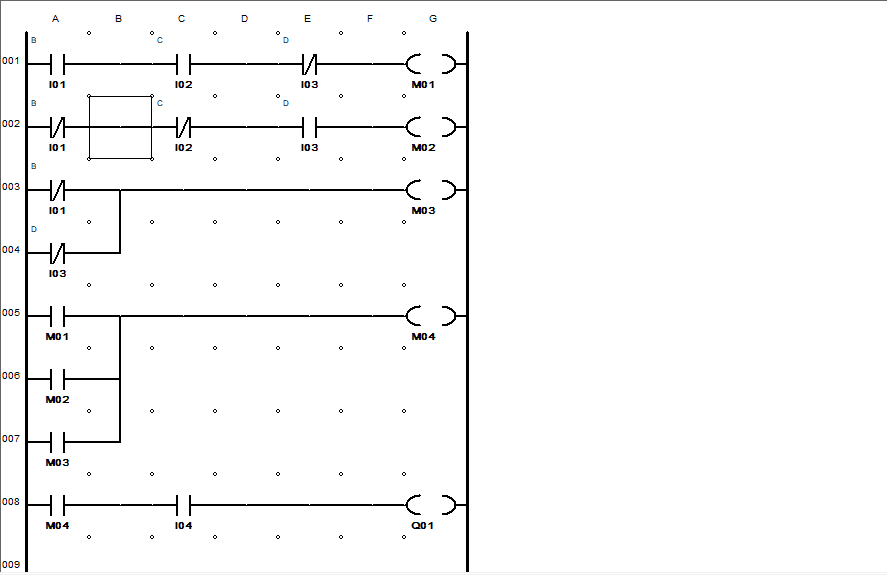
**DOL STARTER**



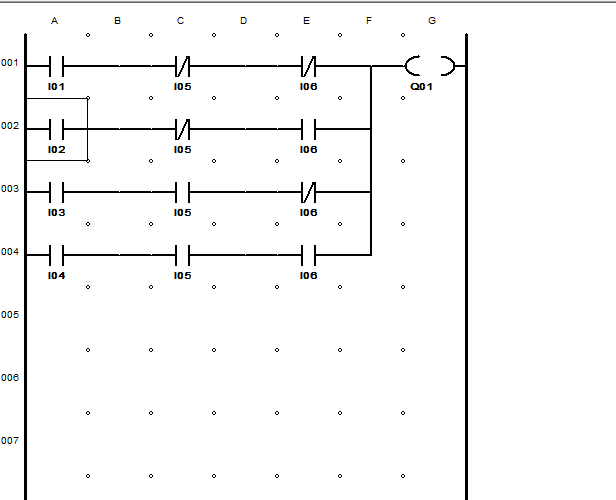
**Equation Solve:**

**Batch4**





**Batch 3**



**4:1 MUX**

**1:4 MUX**

